

U.S. UTILITY Patent Application				PATENT DATE	
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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/756686	F	257	339	2811	Loke Thomas
<p>APPLICANT</p> <p>Kazuo Matsuzaki Naoto Fujishima Akio Kitamura Gen Tada</p>					
<p>TITLE</p> <p>Semiconductor device exhibiting a high breakdown voltage and the method of manufacturing the same</p>					

TERMINAL DISCLAIMER		DRAWINGS			CLAIMS ALLOWED	
		Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims.	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.					NOTICE OF ALLOWANCE MAILED _____ (Assistant Examiner) (Date)	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____ _____					ISSUE FEE _____ (Primary Examiner) (Date) _____ _____	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.					ISSUE BATCH NUMBER _____ _____ _____	
					_____ _____ _____	

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